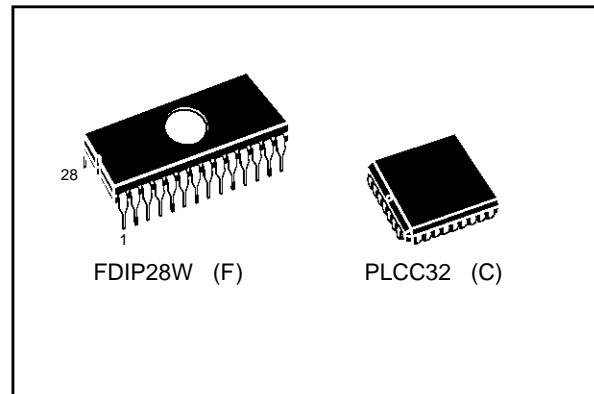


ADDRESS LATCHED 256K (32K x 8) UV EPROM and OTP EPROM

DATA BRIEFING

- INTEGRATED ADDRESS LATCH
- FAST ACCESS TIME: 45ns
- LOW POWER "CMOS" CONSUMPTION:
 - Active Current 30mA
 - Standby Current 100µA
- PROGRAMMING VOLTAGE: 12.75V
- ELECTRONIC SIGNATURE for AUTOMATED PROGRAMMING
- PROGRAMMING TIMES of AROUND 3sec. (PRESTO II ALGORITHM)



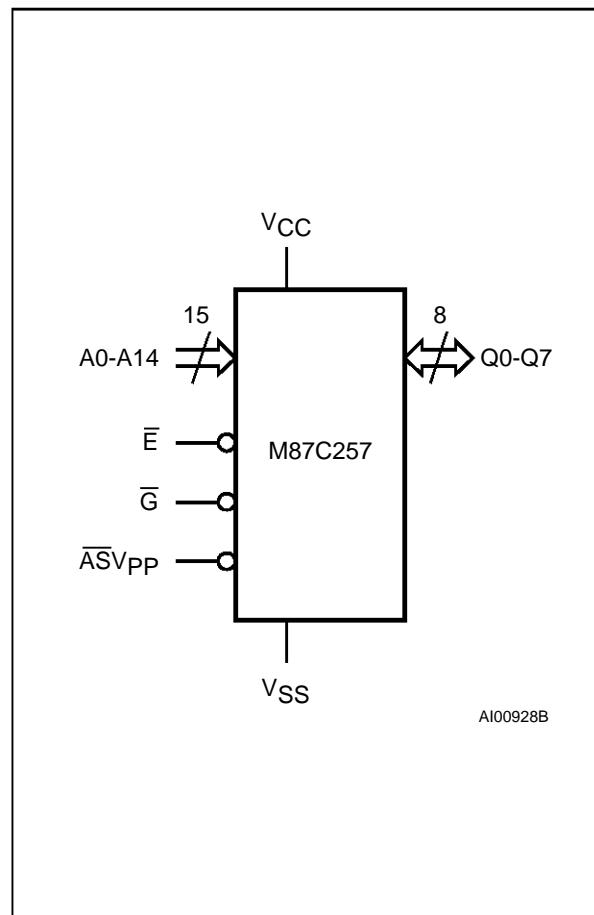
DESCRIPTION

The M87C257 is a high speed 262,144 bit UV erasable and electrically programmable EPROM. The M87C257 incorporates latches for all address inputs to minimize chip count, reduce cost, and simplify the design of multiplexed bus systems.

The Window Ceramic Frit-Seal Dual-in-Line package has a transparent lid which allows the user to expose the chip to ultraviolet light to erase the bit pattern. A new pattern can then be written to the device by following the programming procedure.

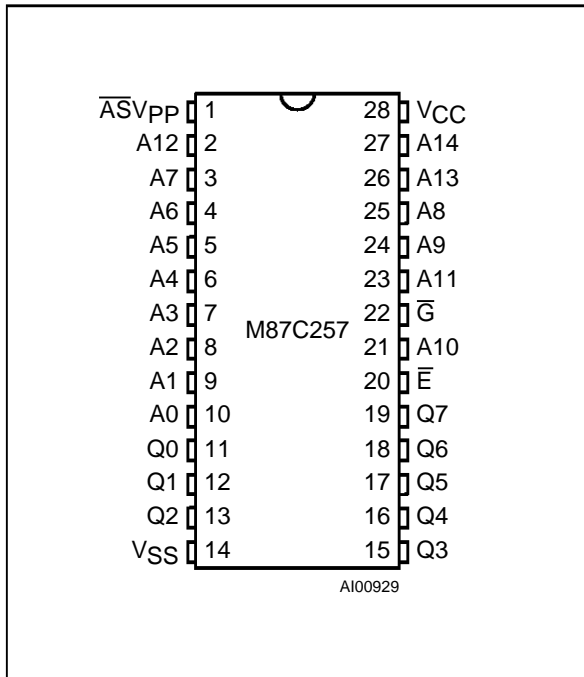
For applications where the content is programmed only one time and erasure is not required, the M87C257 is offered in Plastic Leaded Chip Carrier, package.

Logic Diagram



M87C257

DIP Pin Connections



Signal Names

A0 - A14	Address Inputs
Q0 - Q7	Data Outputs
\bar{E}	Chip Enable
\bar{G}	Output Enable
\bar{ASVPP}	Address Strobe / Program Supply
VCC	Supply Voltage
VSS	Ground

Ordering Information Scheme

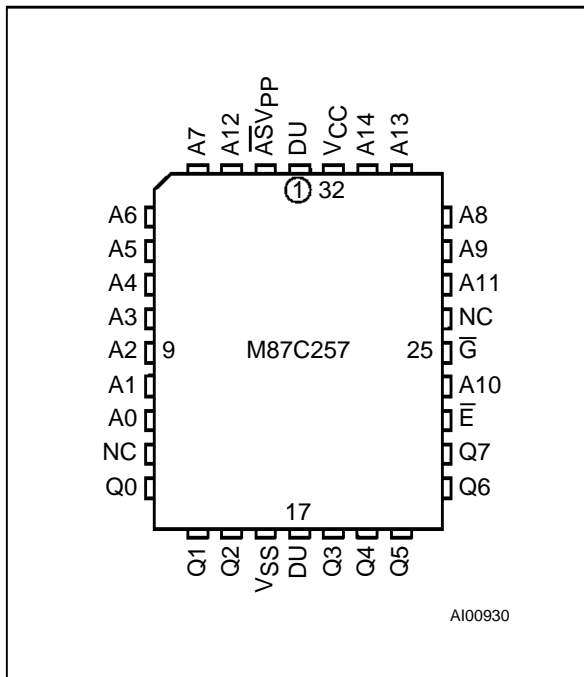
For a list of available options refer to the current Memory Shortform catalogue.

For further information on any aspect of this device, please contact the SGS-THOMSON Sales Office nearest to you.

Example: M87C257 -70 X C 1 X

Speed	-70	X	C	1	X
-45	45ns				
-60	60ns				
-70	70ns				
-80	80ns				
-90	90ns				
-10	100ns				
-12	120ns				
-15	150ns				
-20	200ns				
VCC Tolerance					
X	± 5V				
blank	± 10V				
Package					
F	FDIP28W				
C	PLCC32				
Temp. Range					
1	0 to 70 °C				
6	-40 to 85 °C				
7	-40 to 105 °C				
3	-40 to 125 °C				
Option					
X	Additional Burn-in				
TR	Tape & Reel Packing				

LCC Pin Connections



Warning: NC = Not Connected, DU = Don't Use.